



NILASAILA INSTITUTE OF SCIENCE AND TECHNOLOGY

NH-5, Sergarh-756060, Balasore, Phone No-06782-236003, E mail-nist.bls@gmail.com
APPROVED BY AICTE, NEW DELHI AND AFFILIATED TO SCTE&VT, ODISHA



TIME TABLE

BRANCH: ELECTRICAL ENGINEERING		SEM: 3RD		SESSION: 2023-24		EXAM:2023(W)		W.E.F: 01-08-2023	
DAY	1ST (9.30-10.25)	2ND (10.25- 11.20)	3RD (11.20- 12.15)	4TH (12.15- 01.10)	(01.10- 02.00)	5TH (02.00-02.55)	6TH (02.55-03.50)	7TH (03.50-04.45)	
MONDAY	TH-5	MECHANICAL ENGINEERING LAB (G-1) MECHANICAL WORK SHOP (G-2)				L U N C H B R E A K	TH-3	TH-1	TH-2
TUESDAY	TH-1	TH-4	TH-2	TH-5	MECHANICAL WORKSHOP (G-1) MECHANICAL ENGINEERING LAB(G-2)				
WEDNESDAY	TH-3	CIRCUIT & SIMULATION LAB (G-1) MECHANICAL WORKSHOP(G- 2)					TH-5	TH-4	TH-2
THURSDAY	TH-2	TH-1	TH-3	TH-4	MECHANICAL WORKSHOP (G-1) CIRCUIT & SIMULATION LAB(G-2)				
FRIDAY	TH-5	CIRCUIT & SIMULATION LAB (G-1) MECHANICAL WORKSHOP(G- 2)					TH-1	TH-2	TH-4
SATURDAY	TH-3	TH-1	TH-5	TH-4	PLC DESIGN				

FACULTY LIST SUBJECT WISE :-

TH-1	TH-2	TH-3	TH-4	TH-5
ENGG MATH-III	CIRCUIT & NETWORK THEORY	ELEMENTS OF MECHANICAL ENGG	ELECT. ENGG MATERIAL	ENVIRONMENTAL STUDIES
MR S C DASH	ER. S ROUT	ER B C JENA	ER B PARIDA	ER P BEHERA
PLC				
ER B BEHERA				

Pr-1	Pr-2	Pr-3
MECHANICAL ENGINEERING LAB	CIRCUIT & SIMULATION LAB	MECHANICAL WORKSHOP
ER B C JENA MR A SWAIN	ER N BARIK MR D K DUTTA	ER R GIRI MR S C SAHOO

Copy to:- Concern person/Staff common room/Notice Board/Office

N.B. :-The classes will continue as per the above time table until the further order

Principal
NIST POLYTECHNIC

Principal
Nilasaila Institute of Science & Technology
Sergarh, Balasore



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BRANCH- ELECTRICAL ENGINEERING

SEMESTER-4TH

SESSION-2023-24

W.E.F: 16-01-2024

DAY	1ST (9.30-10.25)	2ND (10.25-11.20)	3RD (11.20-12.15)	4TH (12.15-01.10)	(01.10-02.00)	5TH (02.00-02.55)	6TH (02.55-03.50)	7TH (03.50-04.45)
MONDAY	TH-3	ELECTRICAL DRAWING			L U N C H B R E A K	TH-2	TH-4	TH-1
TUESDAY	TH-1	VLSI DESIGN				ANALOG ELECTRONICS LAB(G-1) ELECTRICAL MACHINE LAB-1 LAB(G-2)		
WEDNESDAY	TH-1	ELECTRICAL MACHINE LAB-1 (G-1) ANALOG ELECTRONICS LAB(G-2)				TH-3	TH-2	TH-4
THURSDAY	TH-1	TH-3	TH-4	TH-2		ELECTRICAL DRAWING		
FRIDAY	TH-1	TH-2	TH-3	TH-4		ELECTRICAL MACHINE LAB-1 LAB(G-1) SIMULATION PRACTICE ON MAT LAB(G-2)		
SATURDAY	TH-1	TH-3	TH-2	TH-4		SIMULATION PRACTICE ON MAT LAB(G-1) ELECTRICAL MACHINE LAB-1 (G-2)		

FACULTY LIST SUBJECT WISE :-

TH-1	TH-2	TH-3	TH-4	Pr-1	Pr-2	Pr-3	Pr-4	
Energy Conversion-I	Analog Electronics & OP-Amp	Electrical Measurement & Instrumentation	Generation, Transmission and Distribution	Electrical Machine Lab-I	Analog Electronics Lab	Simulation Practice on MATLAB	ELECTRICAL DRAWING	VLSI
ER B BEHERA	ER. S ROUT	ER. B PARIDA	ER N BARIK	ER A SWALEHIN	ER P BEHERA MR D K DUTTA	ER. B PARIDA MR A K DAS	ER A SWALEHIN	ER N BARIK

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PRINCIPAL
NIST POLYTECHNIC
15/1/2024

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**TIME TABLE**

BRANCH: ELECTRICAL ENGINEERING

SEM: 5TH

SESSION: 2023-24

EXAM: 2023(W)

W.E.F: 01-08-2023

DAY	1ST (9.30-10.25)	2ND (10.25-11.20)	3RD (11.20-12.15)	4TH (12.15- 01.10)	(01.10- 02.00)	5TH (02.00-02.55)	6TH (02.55-03.50)	7TH (03.50-04.45)
MONDAY	TH-3	TH-1	TH-5	TH-2	L U N C H B R E A K	ELECTRICAL MACHINE LAB-II(G-1) DIGITAL ELECTRONICS & MICROPROCESSOR LAB(G-II)		
TUESDAY	TH-5	PROJECT PHASE-1 (G-1) STUDENT CENTERED ACTIVITIES(G-2)				TH-2	TH-4	TH-1
WEDNESDAY	TH-2	TH-4	TH-1	TH-3		ELECTRICAL MACHINE LAB-II(G-1) POWER ELECTRONICS & PLC LAB(G-II)		
THURSDAY	TH-5	DIGITAL ELECTRONICS & MICROPROCESSOR LAB(G-1) ELECTRICAL MACHINE LAB(G-II)				TH-4	TH-1	TH-3
FRIDAY	TH-2	TH-5	TH-3	TH-4		POWER ELECTRONICS & PLC LAB(G-1) ELECTRICAL MACHINE LAB(G-II)		
SATURDAY	TH-5	TH-2	TH-4	TH-1		VLSI DESIGN		

FACULTY LIST SUBJECT WISE :-

TH-1	TH-2	TH-3	TH-4	TH-5		Pr-1	Pr-2	Pr-3
Entrepreneurship and Management & Smart Technology	Energy Conversion-II	Digital Electronics & Microprocessor	Utilization of Electrical Energy & Traction	Power Electronics & PLC*	Electrical Machine Lab-II	Power Electronics & PLC Lab	Digital Electronics & Microprocessor Lab	Project Phase-I
MR MD MANJUR	ER. B BEHERA	ER. B PARIDA	ER N BARIK	ER A SWALEHIN	BEHERA MR D K D	ER S ROUT MR A K DAS	ER B PARIDA MR D K DUTTA	ER P BEHERA
VLSI								
ER N BARIK								

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BRANCH-ELECTRICAL ENGINEERING			SEMESTER-6TH		SESSION-2023-24		W.E.F:16-01-2024		
DAY	1ST (9.30-10.25)	2ND (10.25-11.20)	3RD (11.20-12.15)	4TH (12.15-01.10)	(01.10-02.00)	5TH (02.00-02.55)	6TH (02.55-03.50)	7TH (03.50-04.45)	
MONDAY	TH-4	TH-2	TH-3	TH-1	L U N C H B R E A K	PROJECT PHASE-II(G-1)ELECTRICAL WORKSHOP(G-2)			
TUESDAY	TH-3	ELECTRICAL WORKSHOP (G-1) PROJECT PHASE-II(G-2)				TH-1	TH-2	TH-4	
WEDNESDAY	TH-3	PROJECT PHASE-II(G-1)ELECTRICAL WORKSHOP(G-2)				TH-4	LIFE SKILL		
THURSDAY	TH-2	TH-4	TH-3	TH-1		ELECTRICAL WORKSHOP (G-1) PROJECT PHASE-II(G-2)			
FRIDAY	TH-4	PROJECT PHASE-II(G-1)ELECTRICAL WORKSHOP(G-2)				TH-1	TH-2	TH-3	
SATURDAY	TH-4	TH-3	TH-2	TH-1		PLC DESIGN			

FACULTY LIST SUBJECT WISE :-

TH-1	TH-2	TH-3	TH-4	Pr-1	Pr-2		
Electrical Installation And Estimating	Switch Gear And Protective Devices	Control System Engineering	Renewable Energy	Electrical Workshop	Project Phase- II	Life Skill	PLC
ER N BARIK	ER B PARIDA	ER S ROUT	ER P BEHERA	ER B BEHERA MR D K DUTTA	ER A SWALEHIN	ER B BEHERA	ER A SWALEHIN

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PRINCIPAL 15/1/2024
NIST POLYTECHNIC

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