



NILASAILA INSTITUTE OF SCIENCE AND TECHNOLOGY
NH-5, Sergarh-756060, Balasore, Phone No-06782-236003, E mail-nist.bls@gmail.com
APPROVED BY AICTE, NEW DELHI AND AFFILIATED TO SCTE&VT, ODISHA



TIME TABLE

BRANCH: ELECTRICAL & ELECTRONICS ENGINEERING					SEM: 3RD	SESSION: 2023-24	EXAM:2023(W)	W.E.F: 01-08-2023	
DAY	1ST (9.30-10.25)	2ND (10.25-11.20)	3RD (11.20-12.15)	4TH (12.15-01.10)	(01.10-02.00)	5TH (02.00-02.55)	6TH (02.55-03.50)	7TH (03.50-04.45)	
MONDAY	TH-5	MECHANICAL ENGINEERING LAB (G-1) MECHANICAL WORK SHOP (G-2)				L U N C H B R E A K	TH-3	TH-1	TH-2
TUESDAY	TH-1	TH-4	TH-2	TH-5	MECHANICAL WORKSHOP (G-1) MECHANICAL ENGINEERING LAB(G-2)				
WEDNESDAY	TH-3	CIRCUIT & SIMULATION LAB (G-1) MECHANICAL WORKSHOP(G-2)					TH-5	TH-4	TH-2
THURSDAY	TH-2	TH-1	TH-3	TH-4	MECHANICAL WORKSHOP (G-1) CIRCUIT & SIMULATION LAB(G-2)				
FRIDAY	TH-5	CIRCUIT & SIMULATION LAB (G-1) MECHANICAL WORKSHOP(G-2)					TH-1	TH-2	TH-4
SATURDAY	TH-3	TH-1	TH-5	TH-4	PLC DESIGN				

FACULTY LIST SUBJECT WISE :-

TH-1	TH-2	TH-3	TH-4	TH-5
ENGG MATH-III	CIRCUIT & NETWORK THEORY	ELEMENTS OF MECHANICAL ENGG	ELECT. ENGG MATERIAL	ENVIRONMENTAL STUDIES
MR S C DASH	ER D OJHA	ER B C JENA	ER R K SETHI	ER A R BEHERA
PLC				
ER N SAHU				

Pr-1	Pr-2	Pr-3
ME LAB	C&S LAB	MECHANICAL W/S
ER B C JENA MR A SWAIN	ER R K PADHI MISS S PANDA	ER R GIRI MR S C SAHOO

Copy to:-Concern person/Staff common room/Notice Board/Office
N.B. :-The classes will continue as per the above time table until the further order

[Signature]
PRINCIPAL
NIST POLYTECHNIC
Principal
Nilasaila Institute of Science & Technology
Sergarh, Balasore



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BRANCH- ELECTRICAL & ELECTRONICS ENGINEERING

SEMESTER-4TH

SESSION-2023-24

W.E.F: 16-01-2024

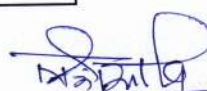
BRANCH- ELECTRICAL & ELECTRONICS ENGINEERING					SEMESTER-4TH	SESSION-2023-24			
DAY	1ST (9.30-10.25)	2ND (10.25-11.20)	3RD (11.20-12.15)	4TH (12.15-01.10)	(01.10-02.00)	5TH (02.00-02.55)	6TH (02.55-03.50)	7TH (03.50-04.45)	
MONDAY	TH-3	ANALOG ELECTRONICS LAB(G-1) ELECTRICAL MACHINE LAB(G-2)			L U N C H B R E A K	TH-2	TH-4	TH-1	
TUESDAY	TH-1	PROGRAMABLE LOGIC CONTROLLER(PLC)				ANALOG ELECTRONICS LAB(G-1) ELECTRICAL MACHINE LAB(G-2)			
WEDNESDAY	TH-1	ELECTRICAL MACHINE LAB(G-1) ANALOG ELECTRONICS LAB(G-2)				TH-3	TH-2	TH-4	
THURSDAY	TH-1	TH-3	TH-4	TH-2		SIMULATION PRACTICE ON MAT LAB(G-1) ELECTRICAL MACHINE LAB(G-2)			
FRIDAY	TH-1	TH-2	TH-3	TH-4		ELECTRICAL MACHINE LAB(G-1) SIMULATION PRACTICE ON MAT LAB LAB(G-2)			
SATURDAY	TH-1	TH-3	TH-2	TH-4		SIMULATION PRACTICE ON MAT LAB(G-1) ELECTRICAL MACHINE LAB(G-2)			

FACULTY LIST SUBJECT WISE :-

TH-1	TH-2	TH-3	TH-4	Pr-1	Pr-2	Pr-3	Pr-4
ELECTRICAL MACHINE	Analog Electronics & OP-Amp	Electrical Measurement & Instrumentation	Generation, Transmission and Distribution	Electrical Machine Lab	Analog Electronics Lab	Simulation Practice on MATLAB	PLC
Er D OJHA	ER R K SETHI	ER P K MOHANTY	ER R K PADHI	ER N SAHU MISS S PANDA	ER E JAYSINGH MR J JENA	ER A R BEHERA MISS S PANDA	ER N SAHU

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PRINCIPAL 15/1/2024
NIST POLYTECHNIC

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TIME TABLE

BRANCH: ELECTRICAL & ELECTRONICS ENGINEERING					SEM: 5TH	SESSION: 2023-24	EXAM:2023(W)	W.E.F: 01-08-2023	
DAY	1ST (9.30-10.25)	2ND (10.25- 11.20)	3RD (11.20- 12.15)	4TH (12.15- 01.10)	(01.10-02.00)	5TH (02.00-02.55)	6TH (02.55-03.50)	7TH (03.50-04.45)	
MONDAY	TH-4	TH-1	TH-2	TH-3	L U N C H B R E A K	POWER ELECTRONICS &PLC LAB(Group-1) DIGITAL ELECTRONICS &MICROPROCESSOR LAB(Group-2)			
TUESDAY	TH-3	TH-4	TH-5	TH-2		DIGITAL ELECTRONICS & MICROPROCESSOR LAB (Group-1) POWER ELECTRONICS & PLC LAB (GROUP-2)			
WEDNESDAY	TH-2	WAVE PROPAGATION &COMMUNICATION ENGG LAB(GROUP-1) ANALOG & DIGITAL COMMUNICATION LAB(GROUP-2)				TH-3	TH-4	TH-5	
THURSDAY	TH-5	TH-1	TH-2	TH-3		PROJECT PHASE-I			
FRIDAY	TH-1	ANALOG&DIGITAL COMMUNICATION LAB(GROUP-1) WAVE PROPAGATION & COMMUNICATION ENGG LAB(GROUP-2)				TH-2	TH-3	TH-4	
SATURDAY	TH-5	TH-2	TH-3	TH-1		TH-4	VLSI DESIGN		

FACULTY LIST SUBJECT WISE :-

TH-1	TH-2	TH-3	TH-4	TH-5
ENTREPRENEURSHIP & MANAGEMENT S. CHAKRABARTY	DIGITAL ELECTRONICS & MICROPROCESSOR	ANALOG & DIGITAL COMMUNICATION	WAVE PROPAGATION & COMMUNICATION ENGG	POWER ELECTRONICS & PLC
MR MD MANZUR	ER R K PADHI	ER R K SETHI	ER N SAHU	ER P K MOHANTY
Pr-4	Pr-5			
WAVE PROPAGATION & COMMUNICATION ENGG	PROJECT PHASE-1	VLSI DESIGN		
ER D OJHA MR J JENA	ER N SAHU	ER D OJHA		

Pr-1	Pr-2	Pr-3
POWER ELECTRONICS & PLC LAB	DIGITAL ELECTRONICS & MICROPROCESSOR LAB	ANALOG & DIGITAL COMMUNICATION LAB
ER E JAYSINGH MISS S PANDA	ER R K PADHI MR J JENA	ER A R BEHERA MISS S PANDA

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 PRINCIPAL 31/7/2023
 NIST POLYTECHNIC
 Principal
 Nilasaila Institute of Science & Technology
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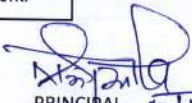
BRANCH-ELECTRICAL & ELECTRONICS ENGINEERING					SEMESTER-6TH	SESSION-2023-24	W.E.F:16-01-2024		
DAY	1ST (9.30-10.25)	2ND (10.25-11.20)	3RD (11.20-12.15)	4TH (12.15-01.10)	(01.10-02.00)	5TH (02.00-02.55)	6TH (02.55-03.50)	7TH (03.50-04.45)	
MONDAY	TH-4	TH-2	TH-3	TH-1	L U N C H B R E A K	ADVANCE COMMUNICATION ENGINEERING LAB (G-1) MAT LAB(G-2)			
TUESDAY	TH-3	PROJECT PHASE-II(G-1)MAT LAB(G-2)				TH-1	TH-2	TH-4	
WEDNESDAY	TH-3	PROJECT PHASE-II(G-1)ADVANCE COMMUNICATION ENGINEERING LAB(G-2)				TH-4	LIFE SKILL		
THURSDAY	TH-2	TH-4	TH-3	TH-1		MAT LAB(G-1) ADVANCE COMMUNICATION ENGINEERING LAB(G-2)			
FRIDAY	TH-4	PROJECT PHASE-II(G-1)MAT LAB(G-2)				TH-1	TH-2	TH-3	
SATURDAY	TH-4	TH-3	TH-2	TH-1		VLSI DESIGN			

FACULTY LIST SUBJECT WISE :-

TH-1	TH-2	TH-3	TH-4	Pr-1	Pr-2	Pr-3	Pr-4
ADVANCE COMMUNICATION ENGG	SWITCH GEAR & PROTECTIVE DEVICE	DIGITAL SIGNAL PROCESSING	RENEWABLE ENERGY SOURCES	ADVANCE COMMUNICATION ENGINEERING LAB	MAT LAB	PROJECT PHASE-II	VLSI DESIGN
ER R K SETHI	ER P K MOHANTY	ER D OJHA	ER R K PADHI	ER N SAHU MISS S PANDA	MR AR BEHERA MR J JENA	ER E JAYSINGH	ER R K SETHI

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