



NILASAILA INSTITUTE OF SCIENCE AND TECHNOLOGY
 NH-5, Sergarh-756060, Balasore, Phone No-06782-236003, E mail-nist.bls@gmail.com
 APPROVED BY AICTE, NEW DELHI AND AFFILIATED TO SCTE&VT, ODISHA



TIME TABLE

BRANCH: ELECTRICAL & ELECTRONICS ENGG				SEM: 3RD	SESSION: 2021-22	EXAM:2021(W)		W.E.F: 01-10-2021	
DAY	1ST (9:15- 10:05)	2ND (10:05 - 10:55)	3RD (10:55 - 11:45)	4TH (11:45 - 12:35)	(12:35 - 1:30)	5TH (01:30 - 02:20)	6TH (02:20 - 03:10)	7TH (3:10 - 4:00)	
MONDAY	TH-5	EME LAB (G-1) MECH. W/S (G-2)			L U N C H B R E A K	TH-3	TH-1	TH-2	
TUESDAY	TH-1	TH-4	TH-2	TH-5		MECH-W/S (G-1) EME LAB(G-2)			
WEDNESDAY	TH-3	CNT LAB (G-1) MECH W/S(G-2)				TH-5	TH-4	TH-2	
THURSDAY	TH-2	TH-1	TH-3	TH-4		MECH-W/S (G-1) CNT LAB(G-2)			
FRIDAY	TH-5	CNT LAB (G-1) MECH W/S(G-2)				TH-1	TH-2	TH-4	
SATURDAY	TH-3	TH-1	TH-5	TH-4		PLC DESIGN			

FACULTY LIST SUBJECT WISE :-

TH-1	TH-2	TH-3	TH-4	TH-5
ENGG MATH-III	CIRCUIT & NETWORK THEORY	ELEMENTS OF MECHANICAL ENGG	ELECT. ENGG MATERIAL	ENVIRONMENTAL STUDIES
MISS P ROY	ER D OJHA	ER M BEHERA	ER H R HATI	MR MD MANJOOR
PLC				
ER N SAHU				

Pr-1	Pr-2	Pr-3
ME LAB	C&S LAB	MECHANICAL W/S
ER M BEHERA	ER S N SANKHUA	ER R GIRI

Copy to:-Concern person/Staff common room/Notice Board/Office

N.B. :-The classes will continue as per the above time table until the further order

PRINCIPAL

NIST POLYTECHNIC

Nilasaila Institute of Science & Technology
Sergarh, Balasore



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CLASS SCHEDULE FOR- ELECTRICAL & ELECTRONICS ENGG.(4TH SEM.) SESSION 2021-22

W.E.F: 14-03-2022

DAY	1ST (9.30-10.25)	2ND (10.25-11.20)	3RD (11.20-12.15)	4TH (12.15-01.10)	(01.10-02.00)	5TH (02.00-02.55)	6TH (02.55-03.50)	7TH (03.50-04.45)
MONDAY	TH-3	AEC LAB(G-1) EC-1 LAB(G-2)			L U N C H B R E A K	TH-2	TH-4	TH-1
TUESDAY	TH-1	PROGRAMABLE LOGIC CONTROLLER(PLC)				AEC LAB(G-1) EC-1 LAB(G-2)		
WEDNESDAY	TH-1	EC-1 LAB(G-1) AEC LAB(G-2)				TH-3	TH-2	TH-4
THURSDAY	TH-1	TH-3	TH-4	TH-2		SIMULATION LAB(G-1) EC-1 LAB(G-2)		
FRIDAY	TH-1	TH-2	TH-3	TH-4		EC-1 LAB(G-1) SIMULATION LAB(G-2)		
SATURDAY	TH-1	TH-3	TH-2	TH-4		SIMULATION LAB(G-1) EC-1 LAB(G-2)		

FACULTY LIST SUBJECT WISE :-

TH-1	TH-2	TH-3	TH-4	Pr-1	Pr-2	Pr-3	Pr-4
ELECTRICAL MACHINE	Analog Electronics & OP-Amp	Electrical Measurement & Instrumentation	Generation, Transmission and Distribution	Electrical Machine Lab	Analog Electronics Lab	Simulation Practice on MATLAB	PLC
Er D OJHA	ER. S SANKHUA	ER H HATI	ER D MAHARANA	ER N SAHU	ER MD HEDAYETULLA	ER A BEHERA	MISS B DAS

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TIME TABLE

BRANCH: ELECTRICAL & ELECTRONICS ENGG SEM: 5TH SESSION: 2021-22 EXAM:2021(W) W.E.F: 01-10-2021									
DAY	1ST (9:15- 10:05)	2ND (10:05 - 10:55)	3RD (10:55 - 11:45)	4TH (11:45 - 12:35)	(12:35 -1:30)	5TH (01:30 - 02:20)	6TH (02:20 - 03:10)	7TH (3:10 - 4:00)	
MONDAY	TH-4	TH-1	TH-2	TH-3	L U N C H B R E A K	POWER ELECTRONICS &PLC LAB(Group-1) DIGITAL ELECTRONICS &MICROPROCESSOR LAB(Group-2)			
TUESDAY	TH-3	TH-4	TH-5	TH-2		DIGITAL ELECTRONICS & MICROPROCESSOR LAB (Group-1) POWER ELECTRONICS & PLC LAB (GROUP-2)			
WEDNESDAY	TH-2	TH-5	TH-1	TH-4		WAVE PROPAGATION &COMMUNICATION ENGG LAB(GROUP-1) ANALOG & DIGITAL COMMUNICATION LAB(GROUP-2)			
THURSDAY	TH-5	TH-1	TH-2	TH-3		PROJECT PHASE-I			
FRIDAY	TH-1	ANALOG&DIGITAL COMMUNICATION LAB(GROUP-1) WAVE PROPAGATION & COMMUNICATION ENGG LAB(GROUP-2)				TH-2	TH-3	TH-4	
SATURDAY	TH-5	TH-2	TH-3	TH-1		TH-4	VLSI DESIGN		

FACULTY LIST SUBJECT WISE :-

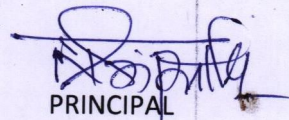
TH-1	TH-2	TH-3	TH-4	TH-5
ENTREPRENEURSHIP & MANAGEMENT	DIGITAL ELECTRONICS & MICROPROCESSOR	ANALOG&DIGITAL COMMUNICATION	WAVE PROPAGATION & COMMUNICATION ENGG	POWER ELECTRONICS& PLC
ER D OJHA	ER H R HATI	ER S N SANKHUA	ER D P MOHARANA	ER N SAHU

Pr-4	Pr-5	
WAVE PROPAGATION & COMMUNICATION ENGG	PROJECT PHASE-1	VLSI DESIGN
ER D P MOHARANA	ER N SAHU	MISS B DAS

Pr-1	Pr-2	Pr-3
POWER ELECTRONICS& PLC LAB	DIGITAL ELECTRONICS & MICROPROCESSOR LAB	ANALOG&DIGITAL COMMUNICATION LAB
ER MD HEDAYTULLA	ER A BEHERA	ER S N SANKHUA

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Principal

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CLASS SCHEDULE FOR ELECTRICAL & ELECTRONICS ENGG(6th SEM) SESSION-2021-22

W.E.F:14-03-2022

DAY	1ST (9.30-10.25)	2ND (10.25-11.20)	3RD (11.20-12.15)	4TH (12.15-01.10)	(01.10-02.00)	5TH (02.00-02.55)	6TH (02.55-03.50)	7TH (03.50-04.45)
MONDAY	TH-4	TH-2	TH-3	TH-1	L U N C H B R E A K	AEC LAB (G-1) MAT LAB(G-2)		
TUESDAY	TH-3	PROJECT PHASE-II(G-1)MAT LAB(G-2)				TH-1	TH-2	TH-4
WEDNESDAY	TH-3	PROJECT PHASE-II(G-1)AEC LAB(G-2)				TH-4	LIFE SKILL	
THURSDAY	TH-2	MAT LAB(G-1) AEC LAB(G-2)				SCA	TH-4	TH-1
FRIDAY	TH-4	PROJECT PHASE-II				TH-1	TH-2	TH-3
SATURDAY	TH-4	TH-3	TH-2	TH-1		VLSI DESIGN		

FACULTY LIST SUBJECT WISE :-

TH-1	TH-2	TH-3	TH-4	Pr-1	Pr-2	Pr-3	Pr-4
ADVANCE COMMUNICATION ENGG	SWITCH GEAR & PROTECTIVE DEVICE	DIGITAL SIGNAL PROCESSING	RENEWABLE ENERGY SOURCES	AEC LAB	MAT LAB	PROJECT PHASE-2	VLSI
ER D OJHA	ER. H HATI	ER. S SANKHUA	ER D MOHARANA	ER N SAHU	MR A BEHERA	ER S SANKHUA	MISS B DAS

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